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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10042308	FILING DATE 01/11/2002	CLASS 35	SUBCLASS 109	GAU 284T	EXAMINER XU
**APPLICANTS: Cooray Nawalage; 1775					
**CONTINUING DATA VERIFIED: None LX					
**FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-5369 01/12/2001 JAPAN 2001-183776 06/18/2001 None LX					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials <i>huy</i>				020026	
TITLE : Dielectric resin composition and multilayer circuit board comprising dielectric layers formed therefrom					
U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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